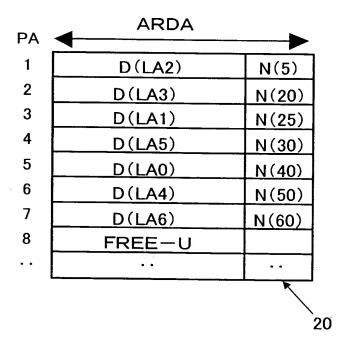
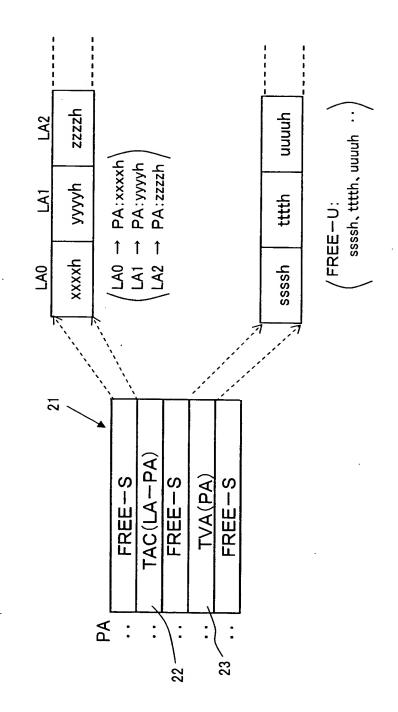
FIG.1 11 51 ₅ 2 MPU **-15 FLASH** CPU 20 IF PGM **FCRL** HST USR WRAM 16 10 ÇCRL - 21 BCRL SYS 13 ′ ARY BUF FMC

FIG.2

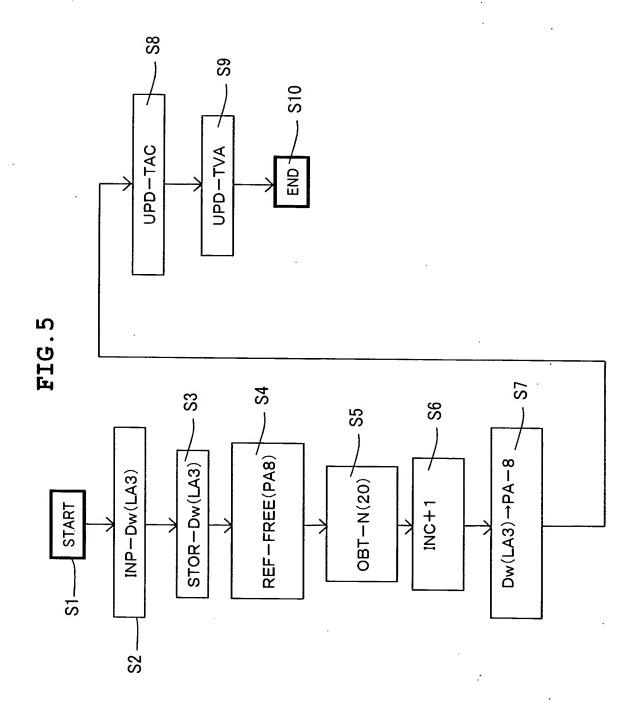




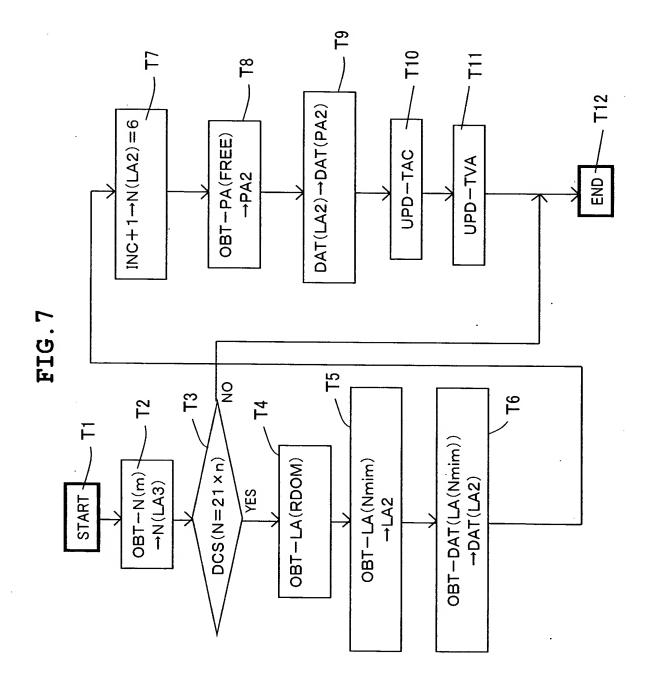


N(25) N(30) N(40) N(50) (09)N N(21) FREE-U ARDAT D(LA2) D(LA5) D(LA0) D(LA4) D(LA6) D(LA1) 1 2 8 4 5 9 7 N(40) N(50) N(60) N(20) N(30) N(25) N(5) ARDAT FREE-L D(LA0) D(LA4) D(LA6) D(LA2) D(LA3) D(LA1) D(LA5) 20 8 4 9 9

20



N(25) N(30) N(40) N(50) N(60) N(6) FREE-U ARDAT D(LA1) D(LA5) D(LA2) D(LA0) D(LA4) D(LA6) D(LA3) N(25) N(30) N(40) N(50) N(60) N(5) ARDAT FREE-U D(LA2) D(LA1) D(LA5) D(LA0) D(LA4) D(LA6) D(LA3) 2 8 4 5 9 7 8



N(5+20) N(25) N(40) N(50) N(30) N(21) N(60) FREE-U D(LA2) D(LA1) **ARDAT** D(LA5) D(LA0) D(LA4) D(LA6) D(LA3) က N(40) N(50) N(60) N(21) N(25) N(30) N(5) ARDAT FREE-U D(LA1) D(LA5) D(LA0) D(LA4) D(LA6) D(LA2) D(LA3) 20 9

